

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	85	mcs-31\$132 or mcs-51\$152	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:29
L2	691	711/220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
L3	4733	8\$1bit near5 (micro\$1processor or micro\$1controller or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
L4	8	L3 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
L5	31008	(increas\$3 or expand\$3 or wid\$3) near5 address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:36
L6	420	3 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:35
L7	4476	(increas\$3 or expand\$3 or wid\$3) adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:36
L8	118	3 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:37
L9	4	8 and ("8051" or mcs-51\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:42

L10	5	(extend\$3 or exapanding) near5 (address adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:57
L11	61	configur\$3 with (address adj (width or size))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:53
L12	1	3 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:53
L13	7521	(extend\$3 or exapad\$3) near5 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:58
L14	4055	(extend\$3 or exapad\$3) near2 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:01
L15	133	3 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:58
L16	1040	(extending or exapading) near2 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:02
L17	15	3 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:07
L18	0	"adding bits to address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08
L19	0	adding adj bits adj to adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08

L20	1	adding adj bits adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08
L25	127	address near5 extender	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:34
L26	3	3 and 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:33
L27	28	address adj extender	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:47
L28	39	3 and "adding bits"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:15
L29	829	concatenat\$3 near5 address near2 bits	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:16
L30	16	3 and 29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:23
L31	39	3 and (adding adj bits)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:29
L32	13	(increasing or expanding ir extending) with (address adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:30
L33	2273	711/211-220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 16:15

L34	1	32 and 33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 16:15
S1	2	"20040172516"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:14
S2	7	tu-li-chun.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:36
S3	12	chen--ping-sheng.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:32
S4	6	S3 not S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:26
S5	4	tseng-pao-ching.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:33
S6	6	kuo-hung-cheng.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:34
S7	4733	8\$1bit near5 (micro\$1processor or micro\$1controller or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:38
S8	4606	stack adj pointer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:38
S9	4897	stack adj pointer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:38

S10	309	S7 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:39
S11	87375	(increas\$3 or expand\$3 or enlarg\$3) near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:35
S12	757	(increas\$3 or expand\$3 or enlarg\$3) near5 internal near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:40
S13	130	S10 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 10:26
S14	6	S10 and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:33
S15	2	adding near2 bits near5 (stack adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 10:02
S16	14	(stack adj pointer) near5 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 10:03
S17	2	S7 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 10:03